**VEERMATA JIJABAI TECHNOLOGICAL INSTITUTE**

**ELECTRICAL ENGINEERING DEPARTMENT**

**Sem : I (ODD 20-21)** Title: **MTech (Electronics)**

**TIME TABLE FOR ONLINE CLASSES**

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| TIME | MONDAY | TUESDAY | WEDNESDAY | THURSDAY | FRIDAY | SATURDAY |
| 09.00-10.00 | VLSI System Design  VDK |  |  |  |  |  |
| 10.05-11.05 | VLSI System Design  VDK | Advanced Communication Theory  DPR | DSP Applications  RAP | DSP Applications  RAP | Advanced Communication Theory  DPR |  |
| 11.15-12.15 | Artificial Neural Network  FSK | DSP Applications  (Practicals)  RAP | Digital System Design  (Practicals)  TAS | VLSI System Design  (Practicals)  VDK | Artificial Neural Network  FSK |  |
| 12.20-13.20 | Artificial Neural Network  FSK | DSP Applications  (Practicals)  RAP | Digital System Design  (Practicals)  TAS | VLSI System Design  (Practicals)  VDK | VLSI System Design  VDK |  |
| 14.00- 15.00 | Digital System Design  TAS | DSP Applications  RAP | Computational Methods  RNA | Computational Methods  RNA | Digital System Design  TAS |  |
| 15.05-16.05 | Computational Methods  RNA | Artificial Neural Network  FSK | Digital System Design  TAS | Advanced Communication Theory  DPR | Computational Methods  RNA |  |
| 16.10-17.10 |  |  |  | Advanced Communication Theory  DPR |  |  |