**VEERMATA JIJABAI TECHNOLOGICAL INSTITUTE**

**ELECTRICAL ENGINEERING DEPARTMENT**

 **Sem : I (ODD 20-21)** Title: **MTech (Electronics)**

 **TIME TABLE FOR ONLINE CLASSES**

|  |  |  |  |  |  |  |
| --- | --- | --- | --- | --- | --- | --- |
| TIME | MONDAY | TUESDAY | WEDNESDAY | THURSDAY | FRIDAY | SATURDAY |
| 09.00-10.00 | VLSI System DesignVDK |  |  |  |  |  |
| 10.05-11.05 | VLSI System DesignVDK | Advanced Communication TheoryDPR | DSP ApplicationsRAP | DSP ApplicationsRAP | Advanced Communication TheoryDPR |  |
| 11.15-12.15 | Artificial Neural NetworkFSK  | DSP Applications(Practicals)RAP | Digital System Design(Practicals)TAS | VLSI System Design(Practicals)VDK | Artificial Neural NetworkFSK  |  |
| 12.20-13.20 | Artificial Neural NetworkFSK  | DSP Applications(Practicals)RAP | Digital System Design(Practicals)TAS | VLSI System Design(Practicals)VDK | VLSI System DesignVDK |  |
| 14.00- 15.00 | Digital System DesignTAS | DSP ApplicationsRAP | Computational MethodsRNA | Computational MethodsRNA | Digital System DesignTAS |  |
| 15.05-16.05 | Computational MethodsRNA | Artificial Neural NetworkFSK  | Digital System DesignTAS | Advanced Communication TheoryDPR | Computational MethodsRNA |  |
| 16.10-17.10 |  |  |  | Advanced Communication TheoryDPR |  |  |