



**Control and Decision Research Centre (CDRC)  
Electrical: Modelling, Computation, and Control  
Lab (EMC<sup>2</sup>)**



Ref No: CDRC/EMC<sup>2</sup>/Admin/ JE&PA/Ad 01

Date: 12 Feb, 2024

**ADVERTISEMENT FOR THE WALK-IN-INTERVIEW  
For the position of  
Junior Engineer (JE) / Project Assistant (PA)  
For EMC<sup>2</sup> Lab**

Veermata Jijabai Technological Institute, Mumbai, is a premier and one of the oldest Engineering Educational Institutes in the country. The CDRC is in search of young and talented engineers to work on various projects for career in research. Walk-In-Interview will be conducted for the appointment of Project Assistant (PA – I, and PA - II) and Junior Engineer (JE) at CDRC, VJTI on 23 Feb 2024 at 4.30 pm in the office of PI, EMC<sup>2</sup> lab in the Electrical Engineering Department, VJTI. The educational qualifications and essential credentials for the posts of “Project Assistant/Junior Engineer “are as below:

Sr. No.	Position	Essential qualification and skills	Job profile requirements	Consolidated stipend per month (INR)
1.	Project Assistant -II	PhD/ME/MTech/MS/MSc + experience	MATLAB / Python / R / Any other relevant application software Knowledge of System Modeling, Analysis/ Stability studies/ Data analysis / Machine learning	45,000.00
2.	Project Assistant - I	Graduate/post Graduate Degree in Professional Course (BE/B. Tech. / ME /MTech/MS/MSc)	Knowledge of Networking and Hands on with Embedded system	40,000.00
3.	Junior Engineer			35,000.00

At the end of the successful completion of the probation period of 3 months, the PI and Co-PI may approve the contention of the services for the total period of 11 months from the date of the appointment. To apply for the position,

1) Fill Google form for the positions of PA-I, PA-II, and JE at E: MC2 lab

<http://tinyurl.com/EMC2JEPA>

2) Get the copy of the detailed CV and relevant documents at the time of interview

3) For any queries, write email to: [pa\\_cdrc@ee.vjti.ac.in](mailto:pa_cdrc@ee.vjti.ac.in)

**SD**

S. R. Wagh PI, EMC<sup>2</sup>

**SD**

Dr. S. K. Bhil, Co-PI, EMC<sup>2</sup>