

Control and Decision Research Centre (CDRC) Electrical: Modelling, Computation, and Control Lab (EMC<sup>2</sup>)



Ref No: CDRC/EMC<sup>2</sup>/Admin/ JE&PA/Ad 02

Date: 2 MAY, 2024

## ADVERTISEMENT FOR THE WALK-IN-INTERVIEW For the position of Junior Engineer (JE) / Project Assistant (PA) For E-MC<sup>2</sup> Lab

Veermata Jijabai Technological Institute, Mumbai, is a premier and one of the oldest Engineering Educational Institutes in the country. The CDRC is in search of young and talented engineers to work on various projects for career in research. Walk–In–Interview will be conducted for the appointment of Project Assistant (PA – I, and PA - II) and Junior Engineer (JE) at CDRC, VJTI on 10 May 2024 at 4.30 pm in the office of PI, EMC<sup>2</sup> lab in the Electrical Engineering Department, VJTI. The educational qualifications and essential credentials for the posts of "Project Assistant/Junior Engineer "are as below:

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Sr.	Position	Essential	Job profile requirements	
No.		qualification and		
		skills		
1.	Project	ME/MTech/MS	MATLAB / Python / R / Any other relevant	
	Assistant -II	+ experience	application software	
2.	Project	Graduate/post	Knowledge of System Modeling, Analysis/	
	Assistant - I	Graduate Degree in	Stability studies/ Data analysis / Machine	
		Professional Course	learning	
3.	Junior	(BE/B. Tech. / ME	Knowledge of Networking and Hands on with	
	Engineer	/MTech/MS/MSc)	Embedded system	
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\* Salary for the above post is between 35,000(INR) to 45,000(INR) based on the performance, which will be discussed in the interview.

At the end of the successful completion of the probation period of 3 months, the PI and Co-PI may approve the contention of the services for the total period of 11

- months from the date of the appointment. To apply for the position,
  1. Scan the QR code to submit the Google form for the positions of PA-I, PA-II, and JE at E-MC^2 lab http://tinyurl.com/EMC2JEPA
  - 2. Get the copy of the detailed CV and relevant documents at the time of interview



3. For any queries, write email to: <u>pa\_cdrc@ee.vjti.ac.in</u>

Sd/-S. R. Wagh PI, EMC<sup>2</sup> Sd/-Dr. S. K. Bhil Co-PI, EMC<sup>2</sup>