



**Veermata Jijabai Technological Institute (V.J.T.I.)**  
**(Central Technological Institute, Maharashtra State, INDIA)**  
**H. R. Mahajani Marg, Matunga, Mumbai 400019**  
**Tel.No. +91 22 24198101-02 Fax: +91 22 24102874**  
**Website: www.vjti.ac.in**

**Programme: Diploma in Electronics Engineering (DEInE)**

**Semester: II**

**Implemented from: 2017**

COURSE CODE	COURSE	GR	TEACHING SCHEME (HRS/WK)				EXAMINATION SCHEME												
			L	T	P	C R	PAPER HRS	TH		IST	TOTAL		PR		OR		TW		TOTAL MARKS
								Max	Min		Max	Min	Max	Min	Max	Min			
173MA21b	Mathematics II	B	3	2		5	3	80	32	20	100	40					25@	10	125
173PH22	Physics	B	3		2	5	3	80	32	20	100	40	25*	10			25@	10	150
173EX23	Electrical Circuits	C	3	1	2	6	3	80	32	20	100	40	50**	20			25@	10	175
173EX24	Basics of Electronics	B	3		3	6	3	80	32	20	100	40	50**	20			25@	10	175
173ME25	Mechanical workshop practice	C	1		3	4											50@	20	50
173EX26	Environmental Studies	B	2			2											5@	20	50
173HM27z	Technical Communication and Presentation Skills	A		2		2											25@	10	25
	Extra co curricular activity	B			2														
	<b>TOTAL</b>		<b>15</b>	<b>5</b>	<b>12</b>	<b>30</b>		<b>320</b>		<b>80</b>	<b>400</b>		<b>125</b>				<b>225</b>		<b>750</b>

Abbreviations: B – Basic; C – Core; A – Applied; M – Management; L – Theory Lecture; T – Tutorial; P – Practical; TH – Theory Paper; IST – In-Semester Test  
 Practical Exam; OR – Oral Exam; TW- Term Work

PR –

\* assessment by Internal Examiner \*\* assessment by External Examiner @ : TW assessment by Internal Examiner

  
**Curriculum Coordinator**

  
**Head**  
**Diploma in Electronics Engg.**

  
**Dean - Diploma**

