



Veermata Jijabai Technological Institute (V.J.T.I)
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Programme: Diploma in Electronics Engineering (DEInE)

Semester: I

Implemented from: 2017

COURSE CODE	COURSE	GR	TEACHING SCHEME (HRS/WK)				EXAMINATION SCHEME												
			L	T	P	C R	PAPER HRS	TH		IST	TOTAL		PR		OR		TW		TOTAL MARKS
								Max	Min		Max	Min	Max	Min	Max	Min			
173MA11a	Mathematics I	B	3	2		5	3	80	32	20	100	40					25@	10	125
173CH12	Chemistry	B	3		2	5	3	80	32	20	100	40	25*	10			25@	10	150
173HM13x	Communication Skill	B	3	2		5	3	80	32	20	100	40					25@	10	125
173EX14	Fundamentals of Electrical and Electronics Engineering	B	3		2	5	3	80	32	20	100	40			25**	10	25@	10	150
173EX15	Electrical components and drawing	B	1		3	4							25**	10			25@	10	50
173ME16	Engineering Graphics	B	2		3	5							50*	20			50@	20	100
173EX17	Computer Application	B			2	2											50@	20	50
	Extra co curricular activity	B			1														
	TOTAL		15	4	13	31		320		80	400		100		25		225		750

Abbreviations: B – Basic; C – Core; A – Applied; M – Management; L – Theory Lecture; T – Tutorial; P – Practical; TH – Theory Paper; IST – In-Semester Test
 Practical Exam; OR – Oral Exam; TW- Term Work.

PR –

* assessment by Internal Examiner ** assessment by External Examiner @: TW assessment by Internal Examiner

Curriculum Coordinator

Head

Dean – Diploma

Diploma in Electronics Engg.

