VEERMATA JIJABAI TECHNOLOGICAL INSTITUTE

TEACHING AND EXAMINATION SCHEME FOR POST S.S.C. DIPLOMA COURSES (R 2013)

COURSE NAME: **DIPLOMA IN ELECTRICAL ENGINEERING**

COURSE CODE:**DEE**

DURATION OF COURSE: 6 SEMESTERS

SEMESTER: FIRST

FULL TIME/PART TIME: FULL TIME

SR. NO	SUBJECT TITLE	SUBJECT	TEACHING SCHEME			EXAMINATION SCHEME												TOTAL
			L	Т	Р	PAPER	TH		TEST	TOTAL		Р		OR		TW		MARKS
						HRS	Max	Min	IESI	Max	Min	Max	Min	Max	Min	Max	Min	
1	Mathematics- I	132MA11a	3	2	-	3	80	32	20	100	40	-	-	-	-	25	10	125
2	Physics	132PH12	3	-	2	3	80	32	20	100	40	25 [@]	10	-	-	25	10	150
3	Communication Skills-I	132HM13x	3	2	-	3	80	32	20	100	40	-	-	-	-	25	10	125
•	Elements of Electrical & Electronics Engineering	132EE14	3	-	2	3	80	32	20	100	40	-	-	25	10	25	10	150
	Mechanical Workshop Practice	132ME15	1	-	3	-	-	-	-	-	-	-	-	-	-	50	20	50
6	Engineering Graphics	132ME16	1	-	3	-	-	-	-	-	-	50	20	-	-	50	20	100
7	Computer Applications	132EE17	-	-	2	-	-	-	-	1	-	-	1	-	-	50	20	50
8	Student Centered Activity / Test		-	-	2													
	TOTAL		14	4	14	-	320	-	80	400	-	75	-	25	-	250	-	750

Student Contact Hours per week (Formal Teaching): 32 Hours

Theory, Practical and Tutorial periods are of 60 minutes duration.

Total Marks: 750

Abbreviations: L- Lecture, T – Tutorials, P – Practical, TH –Theory, OR – Oral, TW- Term work

The @ above-Marks for Practical exams shall be alloted by the Internal Examiner through the conduct of Practical Exams. No External Examiner required.