

VEERMATA JIJABAI TECHNOLOGICAL INSTITUTE

TEACHING AND EXAMINATION SCHEME FOR POST S.S.C. DIPLOMA COURSES (R 2013)

COURSE NAME: **DIPLOMA IN ELECTRICAL ENGINEERING**

COURSE CODE: **DEE**

DURATION OF COURSE: **6 SEMESTERS**

SEMESTER: **SECOND**

FULL TIME/PART TIME: **FULL TIME**

SR. NO	SUBJECT TITLE	SUBJECT CODE	TEACHING SCHEME			EXAMINATION SCHEME												TOTAL MARKS
			L	T	P	PAPER HRS	TH		TEST	TOTAL		P		OR		TW		
							Max	Min		Max	Min	Max	Min	Max	Min			
1	Mathematics- II	132MA21b	3	2	-	3	80	32	20	100	40	-	-	-	-	25	10	125
2	Chemistry	132CH22	3	-	2	3	80	32	20	100	40	25 [@]	10	-	-	25	10	150
3	Communication Skills-II	132HM23z	3	-	2 *	3	80	32	20	100	40	-	-	-	-	50	20	150
4	Basics of Electrical Engineering	132EE24	3	1	3	3	80	32	20	100	40	50	20	-	-	25	10	175
5	Electrical & Electronics Workshop Practice	132EE25	1	-	3	-	-	-	-	-	-	-	-	-	-	50	20	50
6	Electrical & Electronics Drawing	132EE26	1	-	3	-	-	-	-	-	-	-	-	50	20	50	20	100
7	Student Centered Activity / Test		-	-	2													
	TOTAL		14	3	15	-	320	-	70	400	-	75	-	50	-	225	-	750

Student Contact Hours per week (Formal Teaching): 32 Hours

Theory, Practical and Tutorial periods are of 60 minutes duration.

Total Marks : 750

Abbreviations : L- Lecture, T – Tutorials, P – Practical, TH –Theory, OR – Oral, TW- Term work

The @ above-Marks for Practical exams shall be allotted by the Internal Examiner through the conduct of Practical Exams. No External Examiner required.

2 * = Language Laboratory