

VEERMATA JIJABAI TECHNOLOGICAL INSTITUTE

TEACHING AND EXAMINATION SCHEME FOR POST S.S.C. DIPLOMA COURSES (R 2013)

COURSE NAME: **DIPLOMA IN ELECTRICAL ENGINEERING**

COURSE CODE: **DEE**

DURATION OF COURSE: **6 SEMESTERS**

SEMESTER: **FOURTH**

FULL TIME/PART TIME: **FULL TIME**

SR. NO	SUBJECT TITLE	SUBJECT CODE	TEACHING			EXAMINATION SCHEME												TOTAL MARKS
			L	T	P	PAPER HRS	TH		TEST	TOTAL		P		OR		TW		
							Max	Min		Max	Min	Max	Min	Max	Min			
1	Electrical Machines - I	132EE41	3	-	3	3	80	32	20	100	40	50	20	-	-	25	10	175
2	Electrical Networks - II	132EE42	3	1	-	3	80	32	20	100	40	-	-	-	-	25	10	125
3	Integrated Circuits	132EE43	3	-	2	3	80	32	20	100	40	25	10	-	-	25	10	150
4	Power System - I	132EE44	3	2	-	3	80	32	20	100	40	-	-	25	10	25	10	150
5	Instrumentation	132EE45	3	-	2	3	80	32	20	100	40	50	20	-	-	25	10	175
6	'C' Programming	132EE46	2	-	2	-	-	-	-			50	20			50	20	100
7	Development of Life Skills	132EE47	-	-	2	-	-	-	-	-	-	-	-	-	-	25	10	25
8	Student Centered Activity/ Test		-	-	2													
	TOTAL		17	3	13	-	400	-	100	500	-	175	-	25	-	200	-	900

Student Contact Hours per week (Formal Teaching): 33 Hours

Theory, Practical and Tutorial periods are of 60 minutes duration.

Total Marks: 900

Abbreviations : L- Lecture, T – Tutorials, P – Practical, TH –Theory, OR – Oral, TW- Term work