

VEERMATA JIJABAI TECHNOLOGICAL INSTITUTE

TEACHING AND EXAMINATION SCHEME FOR POST S.S.C. DIPLOMA COURSES (R 2013)

COURSE NAME: **DIPLOMA IN ELECTRICAL ENGINEERING**

COURSE CODE: **DEE**

DURATION OF COURSE: **6 SEMESTERS**

SEMESTER: **FIFTH**

FULL TIME/PART TIME: **FULL TIME**

SR. NO	SUBJECT TITLE	SUBJECT CODE	TEACHING			EXAMINATION SCHEME											TOTAL MARKS	
			L	T	P	PAPER HRS	TH		TEST	TOTAL		P		OR		TW		
							Max	Min		Max	Min	Max	Min	Max	Min			
1	Power System - II	132EE51	3	1	-	3	80	32	20	100	40	-	-	-	-	25	10	125
2	Electrical Machines - II	132EE52	3	1	3	3	80	32	20	100	40	50	20	-	-	25	10	175
3	Control System	132EE53	3	-	2	3	80	32	20	100	40	50	20	-	-	25	10	175
4	Microprocessor	132EE54	3	-	3	3	80	32	20	100	40	50	20	-	-	25	10	175
5	Elective- I (Any One)																	
a	PLC & SCADA	132EE55E1	3	-	2	3	80	32	20	100	40			25	10	25	10	150
b	Testing & Maintenance of Electrical Machines	132EE55E2	3		2	3	80	32	20	100	40			25	10	25	10	150
6	Software Skills	132EE56	1		2							-	-	25	10	25	10	50
7	Project	132EE57	-	-	2	-	-	-	-	-	-	-	-	50#	20	50\$	20	100
8	Student Centered Activity / Test		-	-	2													
	TOTAL		16	2	16	-	400	-	100	500	-	150	-	100	-	200	-	950

Students contact hours per week (Formal Teaching) 34 Hours

Theory, Practical and Tutorial periods are of 60 minutes duration.

Total Marks: 950

Abbreviations : L- Lecture, T – Tutorials, P – Practical, TH –Theory, OR – Oral, TW- Term work

\$ - Project Report -50 Marks

- Presentation / Seminar -50 marks