

VEERMATA JIJABAI TECHNOLOGICAL INSTITUTE

TEACHING AND EXAMINATION SCHEME FOR POST S.S.C. DIPLOMA COURSES (R 2013)

COURSE NAME: **DIPLOMA IN ELECTRONICS ENGINEERING**

COURSE CODE:DEInE

DURATION OF COURSE: **6 SEMESTERS**

SEMESTER:**FIRST**

FULL TIME/PART TIME: **FULL TIME**

SR. NO	SUBJECT TITLE	SUBJECT CODE	TEACHING SCHEME			EXAMINATION SCHEME											TOTAL MARKS	
			L	T	P	PAPER HRS	TH		TEST	TOTAL		P		OR		TW		
							Max	Min		Max	Min	Max	Min	Max	Min			
1	Mathematics- I	133MA11a	3	2	-	3	80	32	20	100	40	-	-	-	-	25	10	125
2	Chemistry	133CH12	3	-	2	3	80	32	20	100	40	25 [@]	10	-	-	25	10	150
3	Communication Skills-I	133HM13x	3	2	-	3	80	32	20	100	40	-	-	-	-	25	10	125
4	Elements of Electrical & Electronics Engineering	133EX14	3	-	2	3	80	32	20	100	40	-	-	25	10	25	10	150
5	Electrical & Electronics Workshop Practice	133EX15	1	-	3	-	-	-	-	-	-	-	-	-	-	50	20	50
6	Engineering Graphics	133ME16	1	-	3	-	-	-	-	-	-	50	20	-	-	50	20	100
7	Computer Applications	133EX17	-	-	2	-	-	-	-	-	-	-	-	-	-	50	20	50
8	Student Centered Activity / Test		-	-	2													
	TOTAL		14	4	14	-	320	-	80	400	-	75	-	25	-	250	-	750

Student Contact Hours per week (Formal Teaching): 32 Hours

Theory, Practical and Tutorial periods are of 60 minutes duration.

Total Marks: 750

Abbreviations : L- Lecture, T – Tutorials, P – Practical, TH –Theory, OR – Oral, TW- Term work

The @ above-Marks for Practical exams shall be allotted by the Internal Examiner through the conduct of Practical Exams. No External Examiner required.