

VEERMATA JIJABAI TECHNOLOGICAL INSTITUTE

TEACHING AND EXAMINATION SCHEME FOR POST S.S.C. DIPLOMA COURSES (R 2013)

COURSE NAME: **DIPLOMA IN ELECTRONICS ENGINEERING**

COURSE CODE: **DEInE**

DURATION OF COURSE: **6 SEMESTERS**

SEMESTER: **THIRD**

FULL TIME/PART TIME: **FULL TIME**

SR. NO	SUBJECT TITLE	SUBJECT CODE	TEACHING			EXAMINATION SCHEME												TOTAL MARKS
			L	T	P	PAPER HRS	TH		TEST	TOTAL		P		OR		TW		
							Max	Min		Max	Min	Max	Min	Max	Min			
1	Mathematics- III	133MA31	3	1	-	3	80	32	20	100	40	-	-	-	-	25	10	125
2	Electrical Networks - I	133EX32	3	1	2	3	80	32	20	100	40	50	20	-	-	25	10	175
3	Electronics - I	133EX33	3	-	2	3	80	32	20	100	40	50	20	-	-	25	10	175
4	Measuring Instruments	133EX34	3	-	2	3	80	32	20	100	40	-	-	25	10	25	10	150
5	Digital Integrated Circuits	133EX35	3	-	2	3	80	32	20	100	40	25	10	-	-	25	10	150
6	Electronic Workshop	133EX36	1	-	2							50	20	-	-	50	20	100
7	Product Study & Analysis	133EX37	-	-	2	-	-	-	-	-	-	-	-	-	-	25	10	25
8	Student Centered Activity/ Test		-	-	2													
	TOTAL		16	2	14	-	400	-	100	500	-	175	-	25	-	200	-	900

Student Contact Hours per week (Formal Teaching): 32 Hours

Theory, Practical and Tutorial periods are of 60 minutes duration.

Total Marks : 900

Abbreviations : L- Lecture, T – Tutorials, P – Practical, TH –Theory, OR – Oral, TW- Term work

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