

VEERMATA JIJABAI TECHNOLOGICAL INSTITUTE

TEACHING AND EXAMINATION SCHEME FOR POST S.S.C. DIPLOMA COURSES (R 2013)

COURSE NAME: **DIPLOMA IN ELECTRONICS ENGINEERING**

COURSE CODE: **DEInE**

DURATION OF COURSE: **6 SEMESTERS**

SEMESTER: **FIFTH**

FULL TIME/PART TIME: **FULL TIME**

SR. NO	SUBJECT TITLE	SUBJECT CODE	TEACHING SCHEME			EXAMINATION SCHEME												TOTAL MARKS
			L	T	P	PAPER HRS	TH		TEST	TOTAL		P		OR		TW		
							Max	Min		Max	Min	Max	Min	Max	Min			
1	Analog Communication	133EX51	3	-	3	3	80	32	20	100	40	25	10	-	-	25	10	150
2	Microprocessor	133EX52	3	-	3	3	80	32	20	100	40	50	20	-	-	25	10	175
3	Control System	133EX53	3	-	2	3	80	32	20	100	40	50	20	-	-	25	10	175
4	Power Electronics	133EX54	3	-	3	3	80	32	20	100	40	25	10	-	-	25	10	150
5	Elective- I (Any One)																	
a)	Biomedical Engineering	133EX55E1	3	-	2	3	80	32	20	100	40			25	10	25	10	150
b)	PLC & SCADA	133EX55E2	3	-	2	3	80	32	20	100	40			25	10	25	10	150
6	Software Skills	133EX56			2							-	-	25	10	25	10	50
7	Project	133EX57	-	-	3	-	-	-	-	-	-	-	-	50 [#]	20	50 ^{\$}	20	100
8	Student Centered Activity/ Test		-	-	2													
	TOTAL		15	-	20	-	400	-	100	500	-	150	-	100	-	200	-	950

Student Contact Hours per week (Formal Teaching) 35 Hours

Theory, Practical and Tutorial periods are of 60 minutes duration.

Total Marks: 950

Abbreviations : L- Lecture, T – Tutorials, P – Practical, TH –Theory, OR – Oral, TW- Term Work

\$ - Project Report -50 Marks

- Presentation / Seminar -50 marks