

VEERMATA JIJABAI TECHNOLOGICAL INSTITUTE

TEACHING AND EXAMINATION SCHEME FOR POST S.S.C. DIPLOMA COURSES (R 2013)

COURSE NAME: **DIPLOMA IN ELECTRONICS ENGINEERING**

COURSE CODE: **DEInE**

DURATION OF COURSE: **6 SEMESTERS**

SEMESTER: **SIXTH**

FULL TIME/PART TIME: **FULL TIME**

SR. NO	SUBJECT TITLE	SUBJECT CODE	TEACHING			EXAMINATION SCHEME												TOTAL MARKS
			L	T	P	PAPER HRS	TH		TEST	TOTAL		P		OR		TW		
							Max	Min		Max	Min	Max	Min	Max	Min			
1	Digital Communication	133EX61	3	-	3	3	80	32	20	100	40	50	20	-	-	25	10	175
2	Microcontrollers	133EX62	3	-	3	3	80	32	20	100	40	25	10	-	-	25	10	150
3	Data Communication & Networking	133EX63	3	-	3	3	80	32	20	100	40	-	-	25	10	25	10	150
4	Elective- II (Any One)																	
i)	Advanced Communication	133EX64E1	3	-	3	3	80	32	20	100	40	-	-	25	10	25	10	150
ii)	Embedded System	133EX64E2	3	-	3	3	80	32	20	100	40	-	-	25	10	25	10	150
iii)	Mobile Communication	133EX64E3	3	-	3	3	80	32	20	100	40	-	-	25	10	25	10	150
5	Management Principles	133EX65	2											-	-	25	10	25
6	Project	133EX66	-	-	4	-	-	-	-	-	-	-	-	50 [#]	20	50 ^{\$}	20	100
7	Student Centered Activity/ Test		-	-	2													
	TOTAL		14	-	18	-	400	-	100	400	-	75	-	100	-	175	-	750

Student Contact Hours per week (Formal Teaching): 32 Hours

Theory, Practical and Tutorial periods are of 60 minutes duration.

Total Marks: 750

Abbreviations : L- Lecture, T – Tutorials, P – Practical, TH –Theory, OR – Oral, TW- Term Work

\$ - Project Report -50 Marks

- Presentation / Seminar -50 marks