

V J T I
Veermata Jijabai Technological Institute
 (Central Technological Institute, Maharashtra State, INDIA)
 H. R. Mahajani Marg, Matunga, Mumbai 400019
 Tel.No. +91 22 24198101-02 Fax: +91 22 24102874
 www.vjti.ac.in

FY B TECH	ELECTRICAL ENGINEERING					TIME TABLE	SEM I	A.Y. 25-26
Time / Day	Monday	Tuesday	Wednesday	Thursday	Friday			
08.30 - 09.25	-	-	BTC LAB DL201 G 1 2	CC	CC			
09.30 - 10.25	MATHS TH DL201	-						
10:30 - 11.25	EG TH DL201	BEE LAB G2 / PHYSICS LAB G1	BEE LAB G2 / PHYSICS LAB G1	BEE TH DL001	EG LAB G1 ,2,& G3,4 CCF2			
11.30 - 12.25	BEE TH DL201			BTE TH DL001				
12.25 - 13.30	RECESS							
13.30 - 14.25	PHY TH DL201	PHY TH DL202	DTI LAB Group 1,2, 3 &4	BEE TH DL201	EG TH DL201			
14:30 - 15:25	BTE TH DL201	MATHS TH DL202		MATHS TH DL201	BTC LAB DL201 G 3 4			
15:30 - 16:25	BTC TH DL201	BTE TH DL202		PHY TH DL201				
16.30 - 17.25	-	-	-	-	-			
BTC- Buisness & Technical Communication			BEE- Basics of Electrical Science (Prof. Sunita Premakumar)					
EG- Engineering Graphics			BTE- Basics of Thermal Engg. (Prof. Akshat jain)					
PHY- Applied Physics								